


# The 49th Annual IEEE/ACM International Symposium on Microarchitecture

Co-sponsored by IEEE-CS TC-uARCH and ACM SIGMICRO

The International Symposium on Microarchitecture (MICRO) is the premier forum for the presentation and discussion of *new ideas* in microarchitecture, compilers, hardware/software interfaces, and design of advanced computing and communication systems. The goal of MICRO is to bring together researchers in the fields of microarchitecture, compilers, and systems for technical exchange. The MICRO community has enjoyed having close interaction between academic researchers and industrial designers -- we aim to continue and strengthen this longstanding tradition at the 49th MICRO in Taipei, Taiwan.

We invite original paper submissions on related to but not limited to the following:

- Processor, memory, interconnect, and storage architectures
- Hardware, software, and hybrid techniques for improving system performance, energy-efficiency, cost, complexity, predictability, quality of service, reliability, dependability, security, scalability, programmer productivity, etc.
- Processor, memory, storage, interconnect designs
- Architectures for instruction-level, thread-level, and memory-level parallelism: superscalar, VLIW, data-parallel, multithreaded, multicore, manycore, etc.
- Compiler and microarchitectural techniques for parallelism (ILP, TLP, MLP)
- Low-power, high-performance, and cost/complexity-efficient architectures
- Architectures for emerging platforms, including smartphones, tablets, cloud/datacenter, etc.
- Architectures and compilers for embedded processors, DSPs, GPUs, ASIPs (network processors, multimedia, wireless, etc.)
- Advanced software/hardware speculation and prediction schemes
- Microarchitecture techniques to better support system software, programming languages, programmability, and compilation
- Microarchitecture modeling and simulation methodology
- Insightful experimental and comparative evaluation and analysis of existing microarchitectures, hardware/software mechanisms and workloads

**Important dates:**

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| Abstracts due | : | **April 3rd, 2016** |
| Papers due | : | **April 10th, 2016** |
| Rebuttal period | : | **June 6th - June 10th, 2016** |
| Notifications | : | **June 25th, 2016** |

Submissions should follow the guidelines and formatting rules specified on the conference website. Papers that violate these guidelines and rules may be returned to author(s) without review.

**http://www.microarch.org/micro49/**